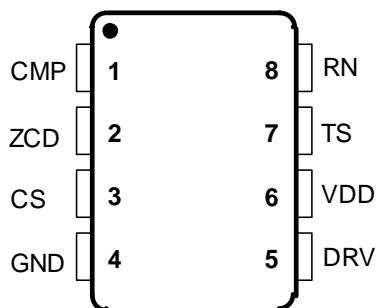


LZC8710

Single Stage buck PFC for LED lighting



Pin Configuration (SOP8)



Absolute Maximum Ratings (Note 1)

- Supply Input Voltage, VDD ----- 30V
- Gate pin----- 30V
- other Pins ----- - 0.3V to 6.5V
- Package Thermal Resistance, θ_{JA}
SOP8 ----- 178°C/W
- Junction Temperature ----- 160°C
- Lead Temperature (Soldering, 10 sec.) ----- 260°C
- Storage Temperature Range ----- -55°C to 150°C
- ESD Susceptibility (Note2)
- HBM (Human Body Mode) ----- 2kV
- MM (Machine Mode) ----- 200V

Recommended Operating Conditions (Note3)

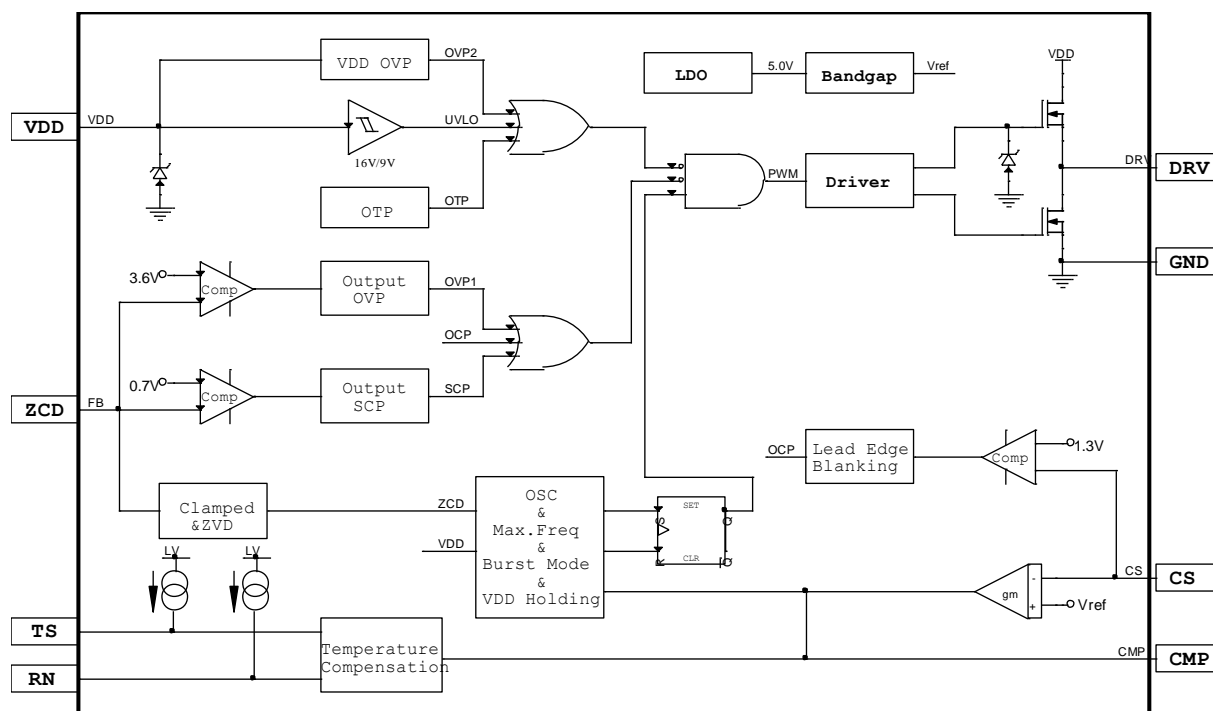
- Junction Temperature ----- -40°C to 125°C
- Ambient Temperature ----- -40°C to 85°C
- Supply Input Voltage, VDD ----- 11V to 23V
- VDD capacitor ----- 10uF to 22uF

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Block Diagram



Pin Assignment

Name	I/O	Pin No.	Description
CMP	I	1	Output of the error amplifier. Connect a RC network across this pin and ground to stabilize the control loop.
ZCD	I/O	2	Inductor current zero crossing detection pin. The pin receives the auxiliary winding voltage or output voltage by a resistor divider and detects the inductor current zero crossing point.
CS	I	3	Connect this pin to the source of the MOSFET. Connect the sense resistor across the source of the MOSFET and the GND pin.
GND	POWER	4	Power Ground.
DRV	O	5	Connect this pin to the gate of MOSFET. Maximum gate voltage is clamping at 16V.
VDD	POWER	6	Power Supply. Provide 27V typical over voltage protection.
TS	I/O	7	Temperature compensation point setting.
RN	I/O	8	Connect NTC resistor for temperature compensation.

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Electrical Characteristics

V_{DD}=15V, T_A=25°C, unless otherwise specified

Symbol	Parameter	Test Conditions	Limits			Unit
			Min.	Typ.	Max.	
VDD Supply Voltage						
V _{DDON}	VDD Turn on threshold		18.1	19.8	21.5	V
V _{DDOFF}	VDD Turn off threshold		7.0	8.0	9.0	V
V _{DDOVP}	Over voltage threshold			27		V
I _{ST}	Startup current	V _{DD} =V _{DDON} -0.3V		5	10	μA
I _{OP}	Operating current of VDD	C _{DRV} =1.5nF		1.5		mA
Oscillator						
F _{MAX}	Maximal Frequency			90		KHz
T _{MAX_ON}	Maximal on time			30		μS
T _{MAX_OFF}	Maximal off time			40		μS
T _{MIN_OFF}	Minimal off time			1.5		μS
ZCD						
V _{ZCD_SINK}	ZCD High clamp voltage	I _{ZCD_SINK_Max} =2mA		6		V
V _{ZCD_SOURCE}	ZCD Low clamp voltage	I _{ZCD_SOURCE_Max} =4mA		0		V
V _{ZCD_SCP}	Output SCP voltage			0.7		V
V _{ZCD_OVP}	Output OVP voltage			3.6		V
T _{ZCD-LEB1}	ZCD pin leading edge blanking time for PRO			1.5		μS
V _{ZCD_DET}	ZCD detection voltage			0.5		V
COMP Compensation						
V _{REF}			247.5	253.0	258.5	mV
I _{COMPH}	Max source current			32		μA
I _{COMPL}	Max sink current			106		μA

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Current Sense (CS)						
T_{CS-LEB}	CS pin Leading edge blanking time			400		nS
$V_{CS-PEAK3}$	Maximum peak voltage	ZCD>1.6V		1.3		V
DRV						
T_R	Rise time of driver	$C_{DRV}=1.0nF$		200		nS
T_F	Fall time of driver	$C_{DRV}=1.0nF$		100		nS
$V_{DRV-MAX}$	DRV clamped voltage			16		V
TS						
I_{TS}	Reference current	$R_{TS}=10k\Omega$	50	100	200	μA
V_{TS_Max}	TS maximum voltage	Open circuit		3.6		V
RN						
I_{RN}	NTC monitoring current	$R_{RN}=10k\Omega$	50	100	200	μA
V_{RN_Max}	RN maximum voltage	Open circuit		3.6		V
K	Current compensation point	$R_{TS}=K * R_{RN}$	1.23	1.25	1.27	N
OTP						
T_{OTPP}	OTP protection temp.			160		$^{\circ}C$
T_{OTPR}	OTP Release temp.			125		$^{\circ}C$

Note 1. Stresses listed as the above “Absolute Maximum Ratings” may cause permanent damage to the device. These are for stress ratings. Functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may remain possibility to affect device reliability.

Note 2. Devices are ESD sensitive. Handling precaution is recommended.

Note 3. The device is not guaranteed to function outside its operating conditions.

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Functional Description

LZC8710 is a constant current Buck PFC controller targeting at LED lighting applications.

The device is designed to operate in transition mode and is suitable for single stage Buck PFC converters in constant on time operation to achieve high power factor.

LZC8710 provides reliable protections such as LED Short Circuit Protection, LED Open Protection, Over Temperature Protection (OTP), etc..

Start up

After AC source or DC BUS is powered on, the capacitor C_{VDD} across V_{DD} and GND pin is charged up by V_{BUS} voltage through a start up resistor R_{ST} . The start-up current of IC is 5uA typical. Once VDD rises up to UVLO(on), the internal blocks start to work. VDD will be pulled down by operation current of IC until the bias supply circuit could supply enough energy to maintain VDD above UVLO(off).

The whole start-up procedure is divided to two figures shown in Fig.1 and Fig.2. t_{STC} is the C_{VDD} charged up section, and t_{STO} is the output voltage built-up section. The start-up time t_{ST} composes of t_{STC} and t_{STO} , and usually t_{STO} is smaller than t_{STC} .

Select the C_{VDD} to get an ideal start up time t_{ST} and ensure the output voltage is built up without another startup.

$$C_{VDD} = \frac{\left(\frac{V_{BUS}}{R_{ST}} - I_{ST}\right)}{UVLO(on)} \quad (1), \quad I_{ST} : \text{start up current.}$$

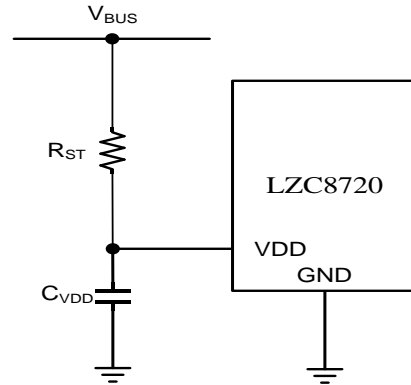


Fig. 1

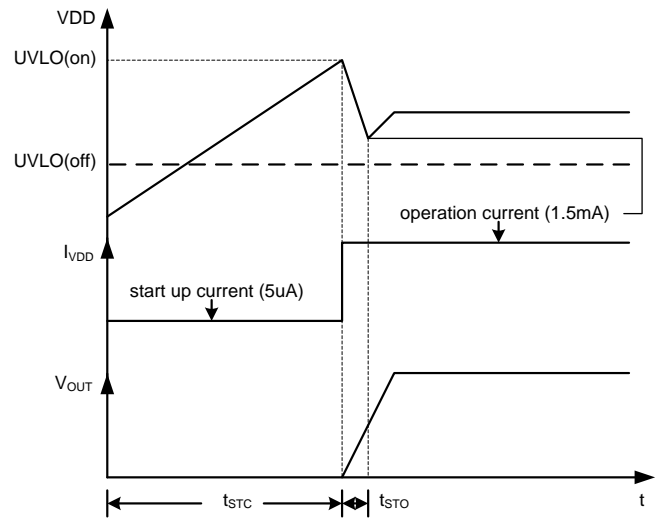


Fig. 2

Zero current detect

The zero current detect (ZCD) generates the turn-on signal of the MOSFET when the inductor current reaches zero current using an auxiliary winding voltage or output voltage that's coupled with the inductor.

If the voltage of the ZCD pin goes higher than 1.6V that its action is ready to aim, the ZCD comparator waits until the voltage goes below 0.7V. And when the voltage goes below 0.5V, the internal logic turns on the MOSFET. That is shown in Fig.3.

The ZCD pin is protected internally by two clamps, 6V high voltage clamp and zero low

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voltage clamp.

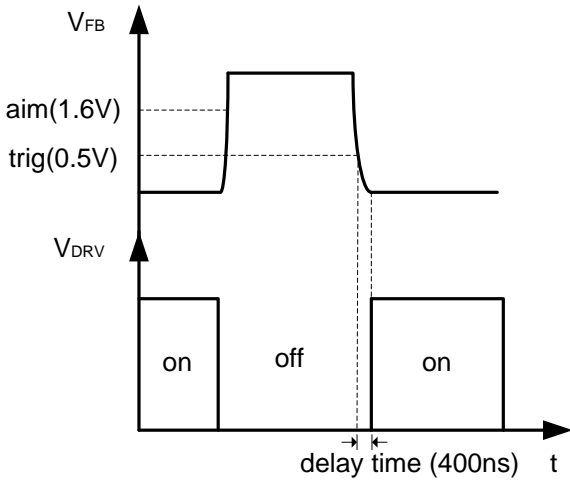


Fig. 3

Switch Driver

The LZC8710 contains a single totem-pole output stage designed for direct drive of the power MOSFET. The drive output is capable of typical rise and fall time of 60ns with 1nF load.

Under voltage lockout

If the VDD voltage reaches UVLO(19.8V), the IC's internal blocks are enabled and start operation. If the VCC voltage drops below 8.0V, most of the internal blocks are disabled to reduce the operating current. VDD voltage should reach UVLO(8.0V) again to do restart up operation.

Constant current control

The output current I_{OUT} can be represented by,

$$I_{out} = \frac{V_{REF}}{R_{CS}} \quad (2)$$

$$V_{REF} : 253mV$$

The figure.4 show the R_{CS} location in Buck PFC converter.

LED Open Protection

For LED lighting applications, when LEDs are opened, the control loop tends to regulate

rated current in the converter. The VDD is connected to output voltage directly. Under this condition, the output voltage will rise to V_{BUS}. At the same time, the output capacitor will be damage because of high voltage

LZC8710 VDD pin has over voltage protection and that voltage is 27V typical. When VDD reach 27V, VDD down to UVLO(off) and restart up again. The auto-recovery function can protect the output capacitor avoid from damage.

LED Short Protection

For LED lighting applications, when a number of LEDs in a string are shorted, the output voltage drops. The VDD is connected to output voltage directly. So VDD drops below UVLO(off) 8.0V and the IC will cease operation.

Once LZC8710 is shutdown, the rectified line voltage will charge the VDD hold-up capacitor via start-up resistor. This is same as start-up operation.

Thermal Shutdown

Thermal shutdown occurs when the IC experiences a junction temperature that is exceeds approximately 160 degree C. It resumes normal operation when the junction temperature drops to or below approximately 125 degree C.

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Typical Application Circuit

- Single stage Buck PFC application circuit

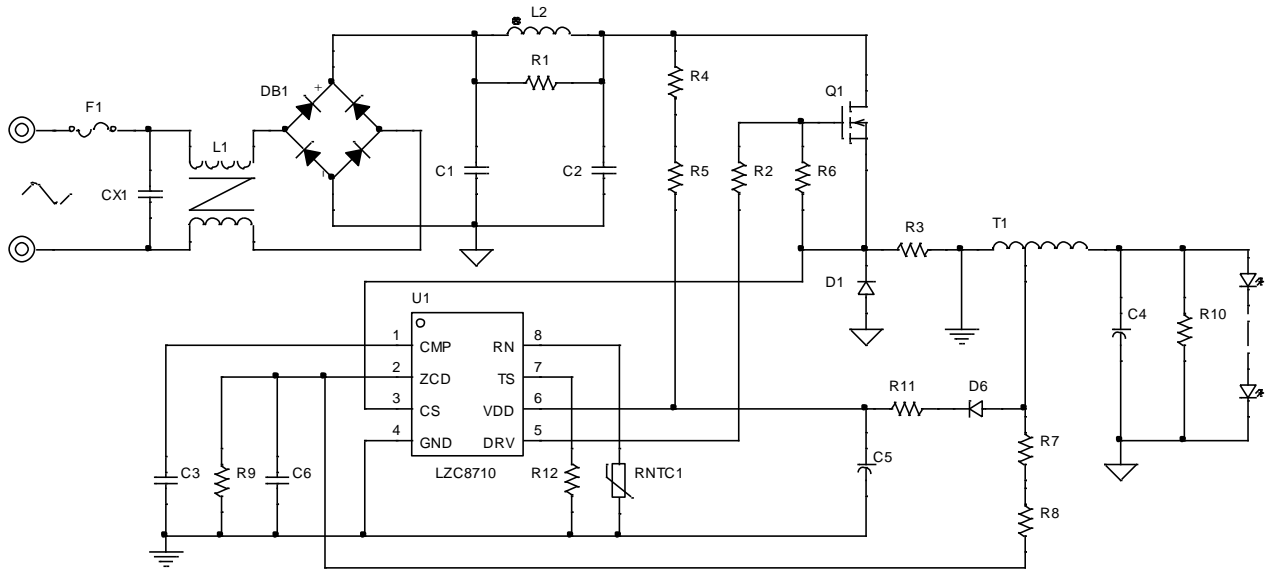


Fig. 4

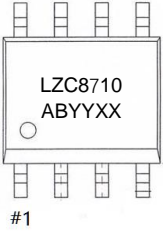
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Ordering & Marking Information

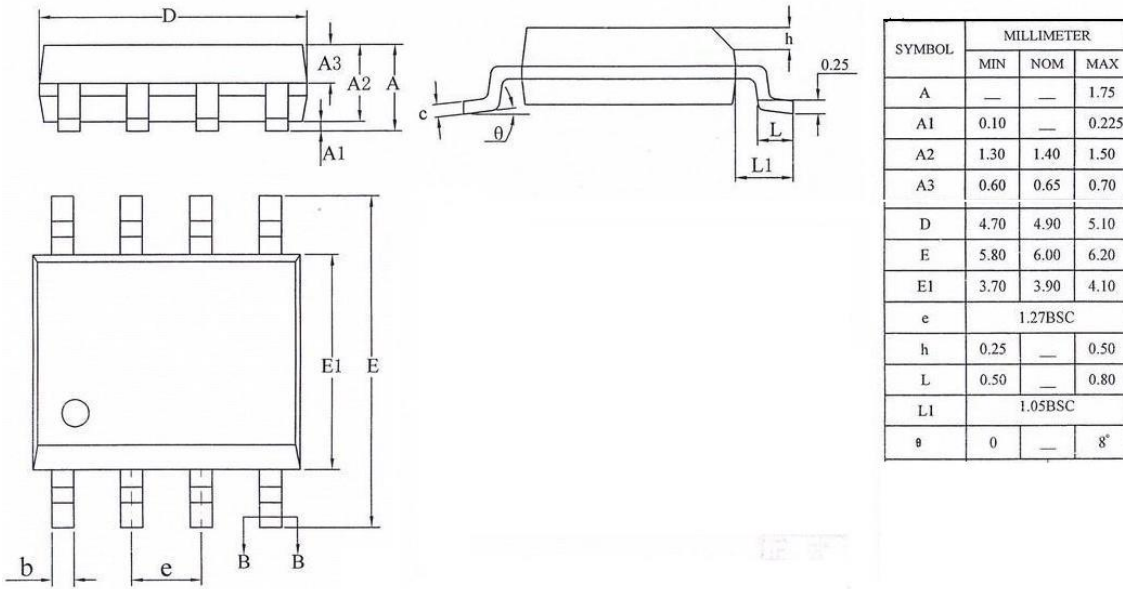
Device Name: LZC8710 for SOP8



→ LZC8710
ABYYXX: AB for Assembly house, YYXX for Date code

#1

Package information



Compliant to JEDEC Standard MS12F

Controlling dimensions are in inches; millimeter dimensions are for reference only

This product is RoHS compliant and Halide free.

Soldering Temperature Resistance:

[a] Package is IPC/JEDEC Std 020D Moisture Sensitivity Level 1

[b] Package exceeds JEDEC Std No. 22-A111 for Solder Immersion Resistance; package can withstand 10 s immersion < 270°C

Dimension D does not include mold flash, protrusions or gate burrs. Mold flash, protrusions or gate burrs shall not exceed 0.15 mm per end. Dimension E1 does not include inter-lead flash or protrusion. Inter-lead flash or protrusion shall not exceed 0.25 mm per side. D and E1 dimensions are determined at datum H. The package top may be smaller than the package bottom. Dimensions D and E1 are determined at the outer most extremes of the plastic body exclusive of mold flash, tie bar burrs, gate burrs and inter-lead flash, but including any mismatch between the top and bottom of the plastic body.